

**DECLARATION (37 CFR 1.63) FOR UTILITY OR DESIGN APPLICATION USING AN  
APPLICATION DATA SHEET (37 CFR 1.76)**

Electronic Version v11

Stylesheet Version v10

<b>Title of Invention</b>	<b>PIPELINE-BASED CIRCUIT WITH A POSTPONED CLOCK-GATING MECHANISM FOR REDUCING POWER CONSUMPTION AND RELATED DRIVING METHOD THEREOF</b>				
<p>As the below named inventor, I declare that:</p> <p>This declaration is directed to the invention titled: " PIPELINE-BASED CIRCUIT WITH A POSTPONED CLOCK-GATING MECHANISM FOR REDUCING POWER CONSUMPTION AND RELATED DRIVING METHOD THEREOF"</p> <p>I believe that I am the original and first inventor of the subject matter which is claimed and for which a patent is sought;</p> <p>I have reviewed and understand the contents of the above-identified application, including the claims, as amended by any amendment specifically referred to above;</p> <p>I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT International filing date of the continuation-in-part application.</p> <p>All statements made herein of my knowledge are true, all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and may jeopardize the validity of the application or any patent issuing thereon.</p>					
<p><b>FULL NAME OF INVENTOR:</b></p> <table border="1" style="width: 100%; border-collapse: collapse;"><tr><td style="width: 50%; padding: 5px;">Inventor: Chung-Hui Chen</td><td style="width: 50%; padding: 5px;">Inventor</td></tr><tr><td style="width: 50%; padding: 5px;">Signature :</td><td style="width: 50%; padding: 5px;">Citizen of : TW</td></tr></table>		Inventor: Chung-Hui Chen	Inventor	Signature :	Citizen of : TW
Inventor: Chung-Hui Chen	Inventor				
Signature :	Citizen of : TW				

As a below named inventor , I hereby declare that :

# PIPELINE-BASED CIRCUIT WITH A POSTPONED CLOCK-GATING MECHANISM FOR REDUCING POWER CONSUMPTION AND RELATED DRIVING METHOD THEREOF

The specification for the above entitled invention was filed previously

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

PRIORITY CLAIM

Claim of priority is based on the following:

P.O. Box 506,  
Merrifield, Virginia 22116,  
U.S.A  
TEL: (02)8923-7350

## DECLARATION

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued hereon.

Date: Nov. 10, 2003

Chung-Hui Chen

Printed Name:

Chung-Hui Chen

Post Office Address:  
and Residence

No. 15, Lane 264, Min-Hsing Rd., Erh-Chung Li,  
Chu-Tung Town, Hsin-Chu Hsien, Taiwan, R.O.C.

Citizen of:

R.O.C.